

End Semester Examination (2021-22)-Odd Semester

M.Tech (CSE) – I Year (I Sem)

Course Name: High Speed Networks

Code:MCS1004

Time: 02 Hours

Max Marks: 60

University Roll No.

(To be filled by the Student)

Note: Please read instructions carefully:

- The question paper has 03 sections and it is compulsory to attempt all sections.
- All questions of Section A are compulsory; questions in Section B and C contain choice.

Section A: Very Short Answer type Questions		BL	CLO	Marks (10)
Attempt all the questions.				
1.	Identify the role of data-link connection identifier (DLCI)?	BL3	CLO 1	02
2.	What do you mean by bandwidth?	BL1	CLO 3	02
3.	Define some WAN technologies.	BL2	CLO 3	02
4.	Differentiate between ARP and RARP protocol.	BL2	CLO 1	02
5.	Define the purpose of CRC.	BL1	CLO 1	02
Section B: Short Answer Type Questions		BL	CLO	Marks (30)
Attempt any 03 out of 06 questions.				
1.	Classify the SVCs (switched virtual circuits) and PVCs (permanent virtual circuits).	BL4	CLO 3	10
2.	Identify the advantages of the following congestion control techniques: a. Back pressure b. Choke packet	BL3	CLO 3	10
3.	Illustrate the terms: propagation delay and transmission delay.	BL4	CLO 3	10
4.	Analyze the advantages and disadvantages of performing congestion control at the transport layer.	BL3	CLO 3	10
5.	Illustrate the role of a cell in ATM network.	BL2	CLO 4	10
6.	Define the Kendall's notation used in the queuing theory.	BL2	CLO 4	10
Section C: Long Answer Type Questions/Case Study		BL	CLO	Marks (20)
Attempt any 01 out of 04 questions.				
1.	Explain the RTP data transfer protocol architecture in detail.	BL6	CLO 3	20
2.	Identify the role of following fields in ATM Cell Structure: (i) Virtual Channel Identifier (ii) Header Error Check	BL6	CLO 1	20
3.	Compare between integrated service architecture and differentiated services.	BL5	CLO 3	20
4.	Summarize the following queuing discipline (i)Fair queuing (ii) Bit round Fair queuing(BRFQ)	BL5	CLO 2	20